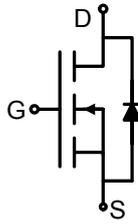
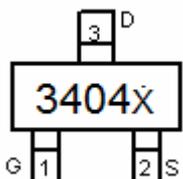


## NCE N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b> The NCE3404 uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math> and low gate charge. This device is suitable for use as a load switch and PWM applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS} = 30V, I_D = 5.8A</math></li> <li>● <math>R_{DS(ON)} &lt; 31m\Omega @ V_{GS}=10V</math></li> <li>● <math>R_{DS(ON)} &lt; 43m\Omega @ V_{GS}=4.5V</math></li> <li>● High Power and current handling capability</li> <li>● Lead free product is acquired</li> <li>● Surface mount package</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Load switch</li> <li>● PWM application</li> </ul>	 <p><b>Schematic diagram</b></p>  <p><b>Marking and pin assignment</b></p>  <p><b>SOT-23 top view</b></p>
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### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3404 X	NCE3404	SOT-23	Ø180mm	8 mm	3000 units

### Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	5.8	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	20	A
Maximum Power Dissipation	$P_D$	1.4	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	89	$^\circ C/W$
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### Electrical Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	30	33	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=30V, V_{GS}=0V$	-	-	1	$\mu A$

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	1.6	2.4	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=5A$	-	25.5	31	m $\Omega$
		$V_{GS}=4.5V, I_D=4A$	-	36	43	m $\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=5V, I_D=5A$	-	15	-	S
<b>Dynamic Characteristics</b> (Note4)						
Input Capacitance	$C_{iss}$	$V_{DS}=15V, V_{GS}=0V,$ $F=1.0MHz$	-	485.8	-	PF
Output Capacitance	$C_{oss}$		-	65.2	-	PF
Reverse Transfer Capacitance	$C_{rss}$		-	54	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=15V, R_L=3\Omega$ $V_{GS}=10V, R_{GEN}=3\Omega$	-	5	-	nS
Turn-on Rise Time	$t_r$		-	3	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	15	-	nS
Turn-Off Fall Time	$t_f$		-	3.5	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=15V, I_D=5.8A,$ $V_{GS}=10V$	-	12.6	-	nC
Gate-Source Charge	$Q_{gs}$		-	1.9	-	nC
Gate-Drain Charge	$Q_{gd}$		-	2.6	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_S=5.8A$	-	-	1.2	V
Diode Forward Current (Note 2)	$I_S$		-	-	5.8	A

### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

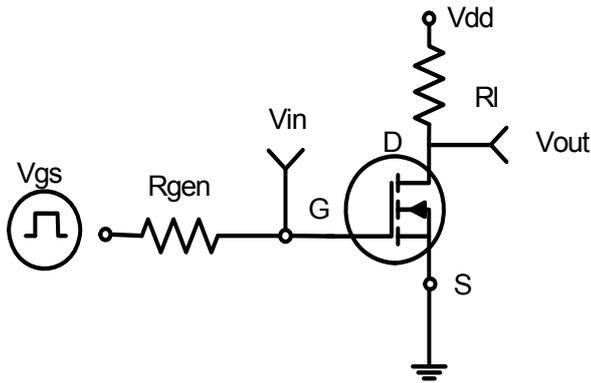


Figure 1: Switching Test Circuit

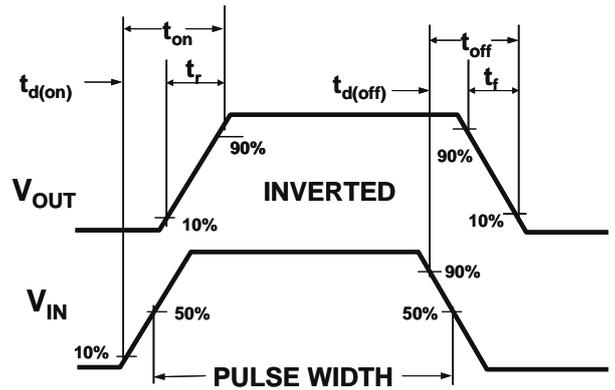


Figure 2: Switching Waveforms

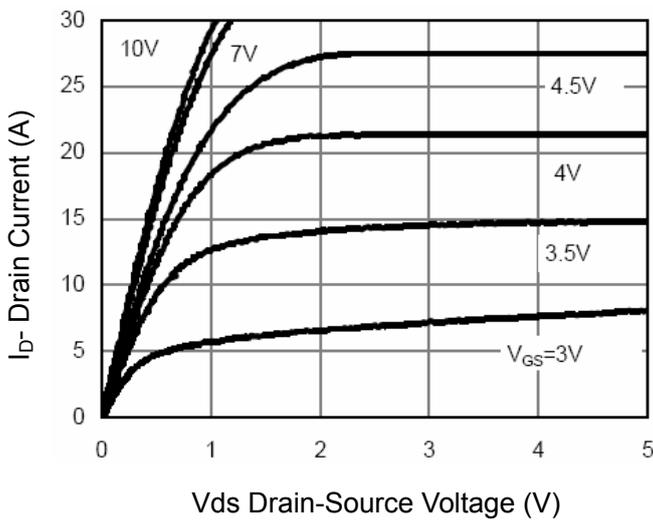


Figure 3 Output Characteristics

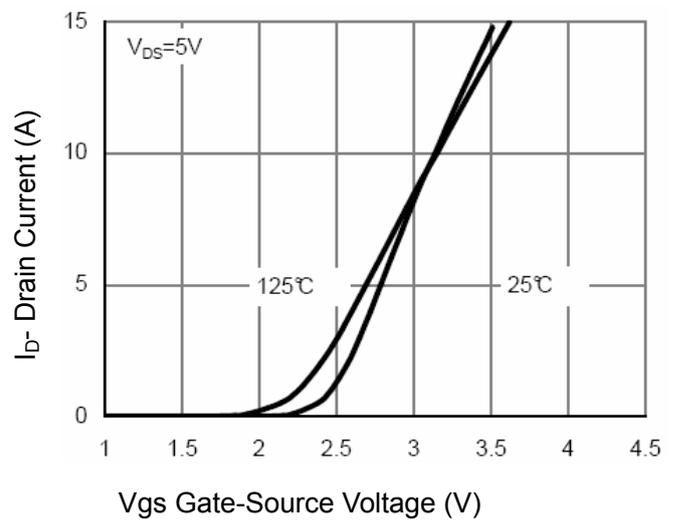


Figure 4 Transfer Characteristics

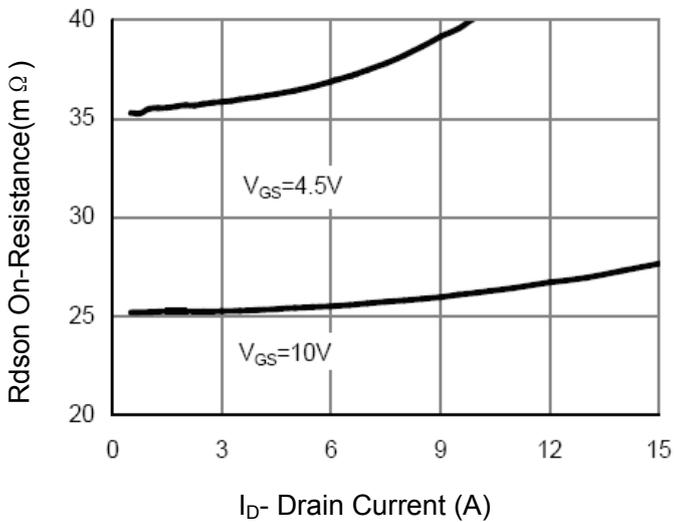


Figure 5 Drain-Source On-Resistance

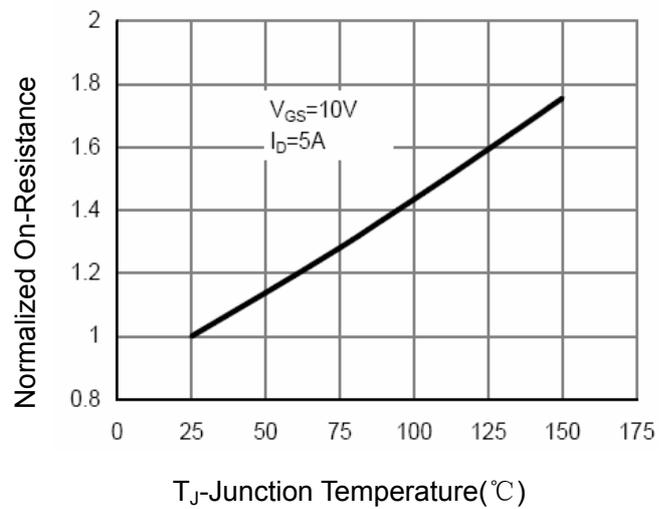
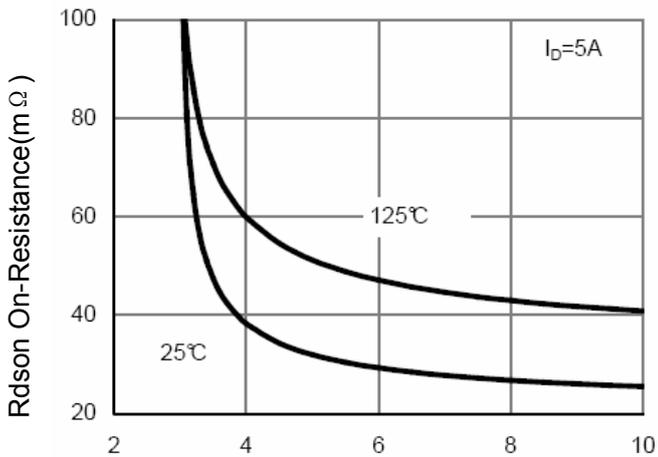
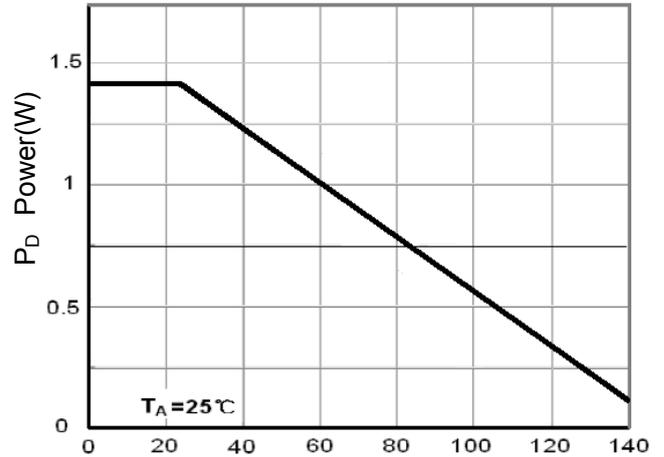


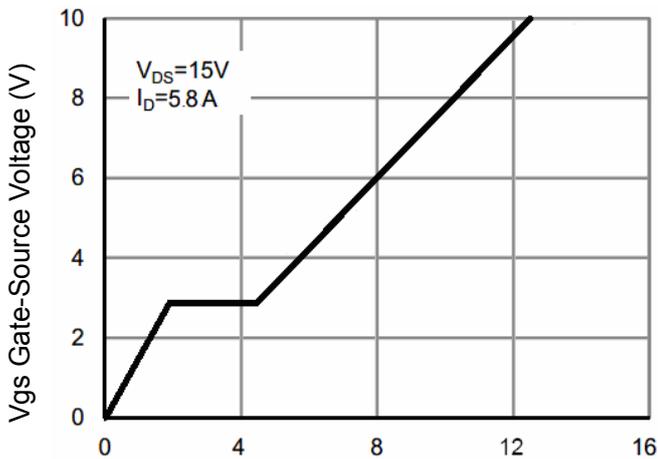
Figure 6 Drain-Source On-Resistance



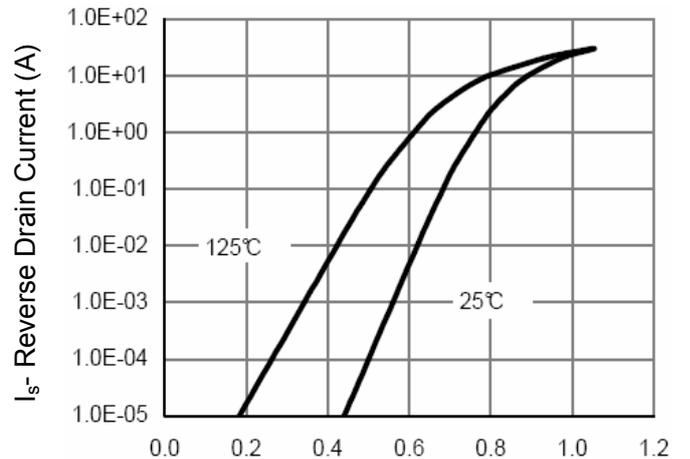
Vgs Gate-Source Voltage (V)  
**Figure 7 Rdson vs Vgs**



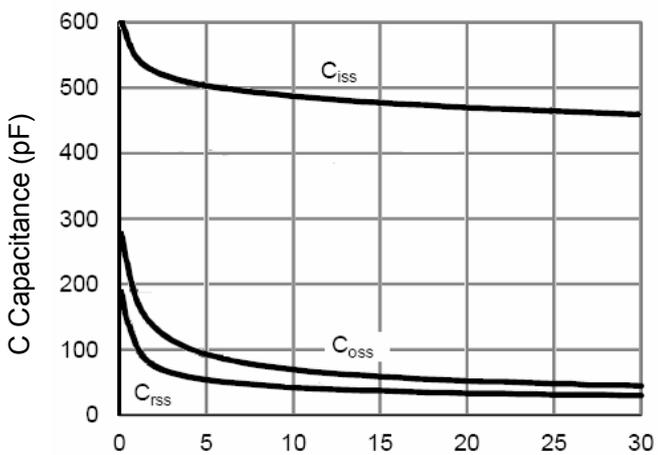
T<sub>J</sub>-Junction Temperature(°C)  
**Figure 8 Power Dissipation**



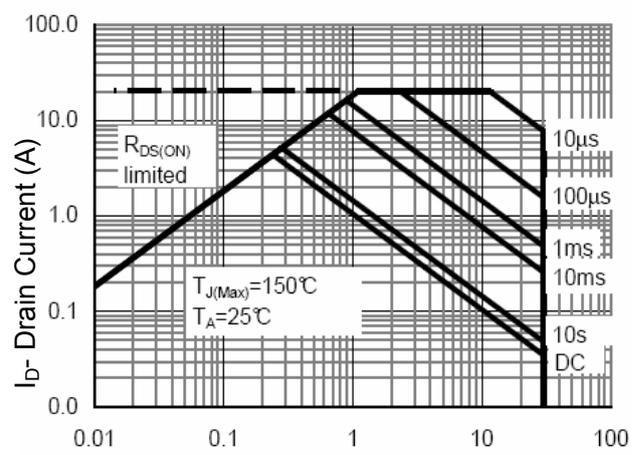
Qg Gate Charge (nC)  
**Figure 9 Gate Charge**



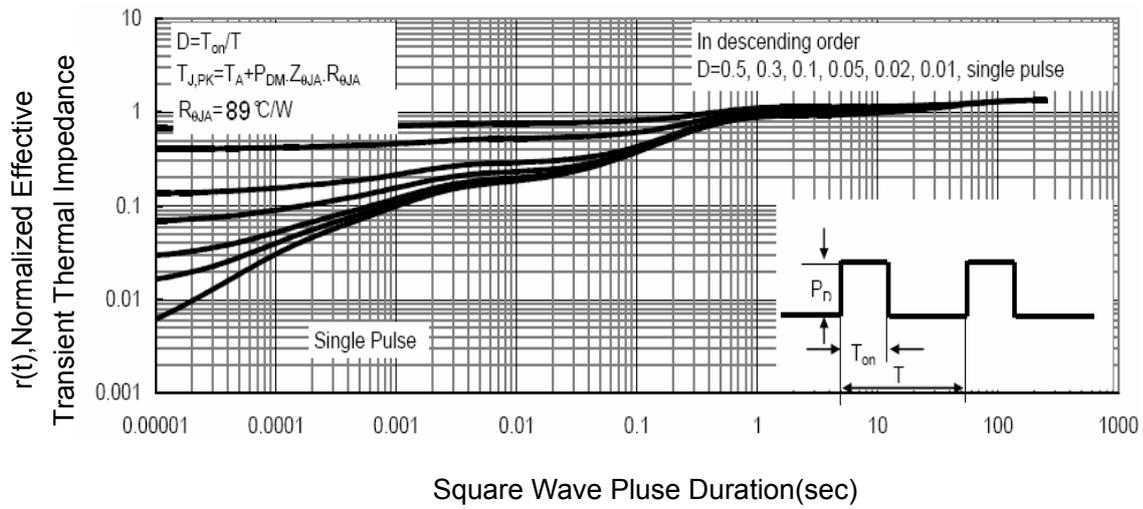
Vds Drain-Source Voltage (V)  
**Figure 10 Source- Drain Diode Forward**



Vds Drain-Source Voltage (V)  
**Figure 11 Capacitance vs Vds**

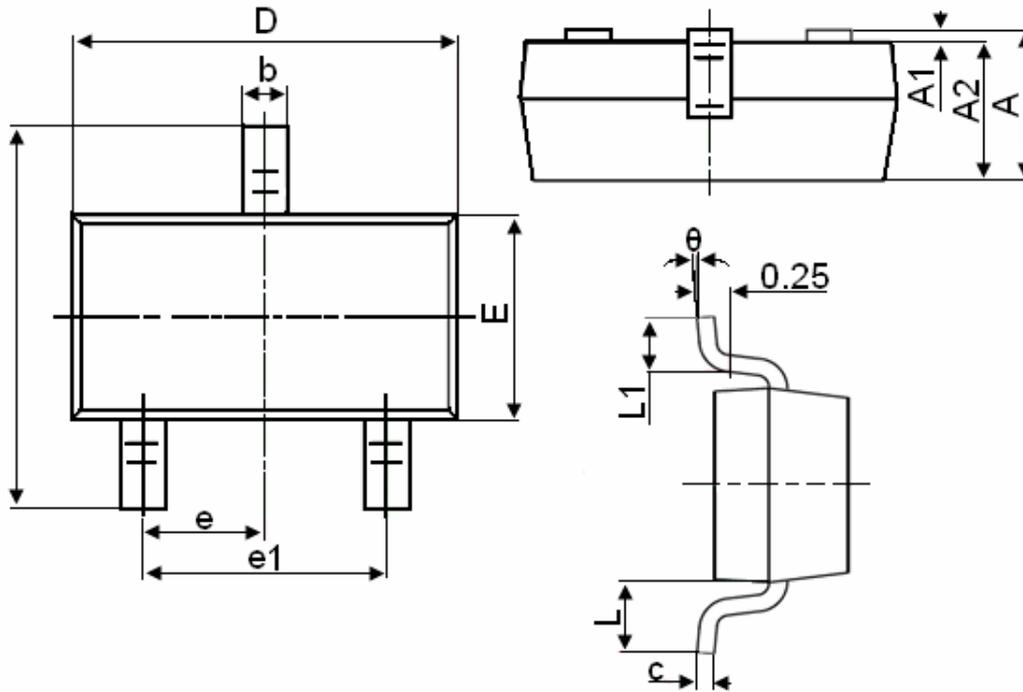


Vds Drain-Source Voltage (V)  
**Figure 12 Safe Operation Area**



**Figure 13 Normalized Maximum Transient Thermal Impedance**

SOT-23 Package Information



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
$\theta$	0°	8°

Notes

1. All dimensions are in millimeters.
2. Tolerance  $\pm 0.10\text{mm}$  (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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